

# Effect of TCSC on Line Voltage Stability Indices under Single Line Outage Condition

Merlyn Mathew<sup>1</sup>, D. Suresh Babu<sup>2</sup>, P.S.Venkataramu<sup>3</sup>, M.S.Nagaraj<sup>4</sup>

M. Tech Scholar, Department of Electrical & Electronics Engineering, LNCTE, Bhopal, India<sup>1</sup>

Asst Professor, Dept of Electrical & Electronics Engg, Sree Vidyanikethan Engineering College, Tirupati, AP, India<sup>2</sup>

Director-IQAC, Reva University, Bangalore, Karnataka, India<sup>3</sup>

Professor& Head, Department of Electrical & Electronics Engineering, BIET, Davangere, Karnataka, India<sup>4</sup>

Abstract: Voltage stability issues have attracted the interest of power system researchers worldwide. It is important to maintain the system stability, or else it would lead to voltage collapse and consequently complete blackout of the system. In this paper the voltage stability indices, Fast Voltage Stability Index (FVSI) and Voltage Reactive Power Index (VQI<sub>Line</sub>) are used to determine the stability of a system. These indices are used to identify the most critical line and bus of the system. Under single line outage condition, effect of placing a TCSC in the system on FVSI & VQI<sub>Line</sub> has been observed. An IEEE 6 bus system has been considered for simulation purpose.

Keywords: TCSC, FVSI, VQI, Enhancement Index.

#### I. **INTRODUCTION**

addressed by the power system engineers and planners today. Because of the increase in load demand the modern power systems are forced to carry increasingly more power over long distances. One of the major problems that may associate with such a stressed system is the voltage instability or collapse. Voltage stability is the ability of a power system to maintain steady voltages at all buses in the system after being subjected to a disturbance from a given initial operating condition [1].

A review of the literature on methods or techniques to study voltage collapse reveals that various analytical tools based on different concepts have been proposed to predict voltage collapse. Several methods have been used for static voltage stability analysis such as the P-V and Q-V curves, modal analysis, etc. [2]. Line voltage stability indices (LVSI) provide important information about the proximity of the system to voltage collapse and are also used to identify the critical line of the system [3]. These indices provide reliable information about the closeness of the system towards voltage instability and the weakest bus and line in the system. An effective solution to improve the voltage profile and voltage stability of the power system is to place an appropriate Flexible AC transmission system (FACTS) controller in the power system. FACTS controller use reliable high speed thyristor based control elements and have the capability of improving power transmission capacity and voltage profile, enhancing power system stability, minimizing transmission losses etc.

#### II. FACTS DEVICES

The benefits of using Flexible AC Transmission system (FACTS) controllers for enhancing power system stability are well known.

Voltage stability is one of the most important issues The use of these controllers gives grid planners and operators a greater flexibility regarding the type of control actions that can be taken at any given time [4]. The utmost requirement of power system is to meet the demand, which varies continuously. That is, the amount of power delivered by the power companies must be equal to that of consumer's need. But events such as faults, tripping of lines etc. causes an imbalance in the reactive power which needs to be compensated. Thus to compensate for the reactive power requirement of the system FACT devices are installed at suitable location. FACT devices offer a number of merits such as fast control characteristics, reactive power compensation increase in dynamic stability, enhances the loading capability of lines and maintains system security.

> The basic function of FACT devices is to control the power flow actively and effectively. In other words, it can transfer power flow from one line to another within its capability.

#### A. TCSC

Thyristor controlled series compensator (TCSC) device is a series compensator which governs the power flow by compensating the reactance of the transmission line. Both capacitive and inductive reactance compensations are possible by proper selection of capacitor and inductor values of the TCSC device.

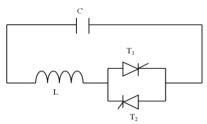


Fig1. A schematic diagram of TCSC



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Fig. 1 shows a TCSC which consists of a series compensating capacitor (C) shunted by a thyristor controlled reactor (TCR) [5].

1) Inductive Mode:

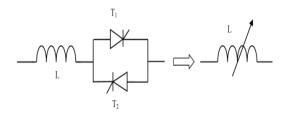


Fig2. TCSC Inductive Mode

TCR is a variable inductive reactor  $X_L(\alpha)$ , tuned at firing angle, as shown in Fig. 2. The variation of  $X_L$  with respect to alpha ( $\alpha$ ) can be given as:

$$X_{L}(\alpha) = X_{L} \frac{\pi}{\pi - 2\alpha - \sin 2\alpha} \dots \dots (1)$$
$$X_{C} = \frac{1}{2\pi fC} \dots \dots (2)$$

For the variation of ( $\alpha$ ) from 0 to 90,  $X_L(\alpha)$  varies from actual reactance ( $X_L$ ) to infinity.

2) Capacitive Mode:

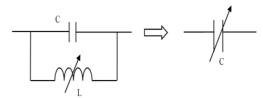


Fig3. TCSC Capacitive Mode

The controlled reactor is connected across the series capacitor, so that the variable capacitive reactance, as shown in Fig. 3, is possible across the TCSC which modifies the transmission line impedance. Effective TCSC reactance  $X_{TCSC}$  with respect to alpha ( $\alpha$ ) can be given as:

$$X_{\text{TCSC}}(\alpha) = -X_{\text{C}} + C_1 (2(\pi - \alpha) + \sin(2(\pi - \alpha))) - C_2 \cos^2(\pi - \alpha) (\omega \tan(\omega(\pi - \alpha))) - \tan(\pi - \alpha)) \dots (3)$$

Where,

$$C_{1} = \frac{X_{C} + X_{LC}}{\pi} \dots (4)$$

$$C_{2} = 4 \frac{X_{LC}^{2}}{X_{L}\pi} \dots (5)$$

$$X_{LC} = \frac{X_{L}X_{C}}{X_{C} - X_{L}} \dots (6)$$

$$\omega = \sqrt{\frac{X_{C}}{X_{L}}} \dots (7)$$

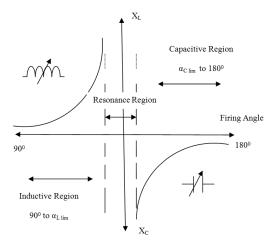


Fig4. TCSC reactance Vs firing angle characteristics curve

#### **III. LINE VOLTAGE STABILITY INDICES**

The condition of voltage stability in a power system can be known using voltage stability indices. These indices either reveal the critical bus of a power system or the stability of each line connected between two buses in an interconnected network and evaluates the voltage stability margins of the system. The indices are discussed below:

A. Fast Voltage Stability Index FVSI

The line stability index FVSI proposed by I.Musirin et al. [6] is based on the concept of power flow through a single line. For a typical transmission line, the stability index is calculated as:

Let us consider a simple two-bus power system model:

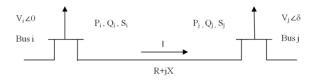


Fig 5 .Two bus system for FVSI formulation

$$I = \frac{V_i \angle 0 - V_j \angle \delta}{R + jX} \dots \dots (8)$$

$$I_j = \frac{P_j - JQ_j}{V_j \angle -\delta} \dots \dots \dots (9)$$

Equating (8) & (9)

$$V_i V_j \angle -\delta - V_j^2 = (R + jX) (P_j - jQ_j) \dots \dots (10)$$

Hence a quadratic equation of  $V_i$  can be formed:

$$V_j^2 + V_i V_j \left(\frac{R \sin \delta}{X} - \cos \delta\right) + Q_j \left(\frac{R^2 + X^2}{X}\right)$$
  
= 0 ..... (11)

For real values to exist for  $V_j$  there must be real roots of the equation

$$V_i^2 \left(\frac{Rsin\delta - Xcos\delta}{X}\right)^2 - 4Q_j \left(\frac{R^2 + X^2}{X}\right) \ge 0 \dots (12)$$



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The expression has real roots only if

$$\frac{4Z^2Q_jX}{V_i^2(R\sin\delta + X\cos\delta)^2} \le 1.0\dots(13)$$

By assuming that  $\delta \approx 0$ ,  $\approx 0$ , *Xcos* $\delta \approx X$ , we can define the fast voltage stability index for line i - j

$$FVSI_{ij} = \frac{4Z^2Q_j}{V_i^2X}\dots\dots(14)$$

Where

Z = line impedence

*X*= line reactance

 $Q_i$  = reactive power flow at the receiving end

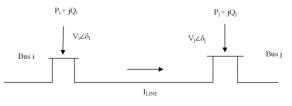
 $V_i$  = sending end voltage

When the FVSI of a line approaches unity it means that the line is approaching its stability limits. The FVSI of all the lines must be lower than 1 to assure the stability of the power system. The line that gives index value closest to 1 will be the most critical line of the bus and may lead to the whole system instability. The calculated FVSI can thus be used to determine the weakest bus of the system.

#### B. Voltage Reactive Power Index VQILine

F.A. Althowibi [7] proposed an index  $VQI_{line}$  through power flow equations. The line with index value closer to 1 is considered as the line which is more prone to instability and can cause the entire system to collapse.

Consider a two bus single line system as illustrated in Fig.6, which can be extended to an n-line power system.



#### Fig6. A two bus system

Where:

 $V_i, V_j$  = sending and receiving voltages at system buses  $\delta_i, \delta_j$  = sending and receiving voltages angle at system bus i and j

 $P_i, P_i$  = sending and receiving end real power

 $Q_i, Q_j$  = sending and receiving reactive power

 $Y_{ij} = (G + jB)$  line admittance between bus k and m

 $\theta$  = line admittance angle

r + jx = line impedance between bus i and j

When bus i is taken as a reference bus, the line current  $I_{line}$  is calculated by:

The current  $I_{line}$  can also be determined by using the receiving end power at bus j, given as,

$$I_{line} = \left(\frac{s_j}{v_j}\right)^* = \frac{(P_j - jQ_j)}{v_j \angle -\delta_j}....(16)$$

Rearranging equation (15) & (16) yields:

$$P_j - jQ_j = |V_j V_i Y_{ij}| \angle (\theta - \delta_j) - |V_j|^2 \cdot |Y_{ij}| \angle \theta \dots (17)$$

The real and imaginary parts can be separated from equation (17) as:

$$\operatorname{Re:} P_{j} = |V_{j}V_{i}Y_{ij}| \angle (\theta - \delta_{j}) - |V_{j}|^{2} \cdot |Y_{ij}| \cdot \cos(\theta) \dots \dots (18)$$
$$\operatorname{Im:} Q_{j} = |V_{j}V_{i}Y_{ij}| \angle (\theta - \delta_{j}) - |V_{j}|^{2} \cdot |Y_{ij}| \cdot \sin(\theta) \dots \dots (19)$$

Substituting equation (18) into equation (17) to establish a relationship between  $V_i$  and  $Q_j$  yields:

$$VQI_{Line} = \frac{4Q_j X_{ij}}{V_i^2} \dots \dots (20)$$

 $Q_j$  = reactive power flow at the receiving end  $V_i$  = sending end voltage

$$\begin{aligned} |V_{j}V_{i}Y_{ij}| \angle (\theta - \delta_{j}) - |V_{j}|^{2} \cdot |Y_{ij}| \cdot \cos(\theta) - jQ_{j} \\ &= |V_{j}V_{i}Y_{ij}| \angle (\theta - \delta_{j}) \\ - |V_{j}|^{2} \cdot |Y_{ij}| \angle \theta \dots (21) \end{aligned}$$
$$\begin{aligned} |V_{j}|^{2} - |V_{j}V_{i}| \frac{(\sin(\theta - \delta_{j}))}{(\sin(\theta))} + \frac{Q_{j}}{|Y_{ij}|(\sin(\theta))} = 0 \dots (22) \end{aligned}$$

Since  $\delta_j$  is very small, it is assumed to be zero seeking equation simplification, then the whole term of  $\frac{(sin(\theta - \delta_j))}{(sin(\theta))}$  is eliminated and yields,

$$|V_j|^2 - |V_ji| + \frac{Q_j}{|Y_{ij}|(sin(\theta))} = 0 \dots (23)$$

Since, $B_{ij} = Y_{ij} \sin(\theta)$ , the new equation can be rewritten as,

$$|V_j|^2 - |V_j V_i| + \frac{Q_j}{|B_{ij}|} = 0 \dots (24)$$

The roots of  $V_i$  is expressed as:

$$V_{j} = \frac{+|V_{i}| \mp \sqrt{|V_{i}|^{2} - \frac{4 \cdot Q_{j}}{|B_{ij}|}}}{2} \dots \dots (25)$$

Eq. (25) gives one distinct real root or two equal roots of  $V_j$  if discriminate ( $\Delta = b^2 - 4ac$ ) is zero. The discriminated roots of  $V_i$  can be expressed as

$$\begin{cases} |V_i|^2 - \frac{4 \cdot Q_j}{|B_{ij}|} \le 0\\ \frac{4 \cdot Q_j}{|B_{ij}| \cdot |V_i|^2} \le 1 \end{cases} \dots \dots \dots (26)$$

 $V_j$  varies from zero to one indicating the real root limitation and can be used as the voltage stability limits. The voltage real roots must be greater than zero and lower than one, otherwise the voltage stability is to be compromised; this proves that the developed equation determines voltage stability at each line and predicts system voltage collapse, and is thus named as voltage reactive power index of line, VQI<sub>Line</sub>, and given as :

$$VQI_{Line} = \frac{4. Q_j}{|B_{ij}| \cdot |V_i|^2} \le 1.0 \dots (27)$$

Thus, if the value of VQI<sub>Line</sub> approaches unity, the voltage stability reaches its stability limits. Voltage instability occurs when VQI<sub>Line</sub> is beyond unity. VQI<sub>Line</sub> determines how far the power system is from instability or collapse point.

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## **IV. PROPOSED ALGORITHM**

- Read the system data
- Run the load flow without line outage contingency and use the results as base case
- Connect N-1 line outage contingency between any two buses and obtain load flow results
- $\bullet$  Calculate the values of FVSI and VQI\_{Line} from the B. VQI\_{Line} equations (14) & (27)
- Rank the weakest / most critical line from the base case and line outage contingency condition having highest value of FVSI and VQI<sub>Line</sub>
- Connect TCSC at the most critical line i.e. between load buses (neglecting the generator buses).
- Obtain the results with TCSC connected in the line for each inductive and capacitive mode of 10% to 50% compensation.
- · Compare the calculated values of indices with and without TCSC
- Calculate the % enhancement of the indices as given in section 5.

#### V. ENHANCEMENT INDEX

The enhancement index can be calculated which determines the percentage enhancement observed in the transmission line after placement of TCSC. The enhancement index can be defined as:

% Enhancement Index = 
$$\frac{\text{VSI}_{\text{Base}} - \text{VSI}_{\text{TCSC}}}{\text{VSI}_{\text{TCSC}}} \times 100$$

Where VSI<sub>Base</sub> is Voltage Stability Index without TCSC VSI<sub>TCSC</sub> is voltage stability index with TCSC VSI<sub>Base</sub> is any index i.e. FVSI or VQI<sub>Line</sub>

### VI. CASE STUDY & RESULTS

To demonstrate the effectiveness of methodology an IEEE 6 bus system is considered. This system consists of 11 transmission lines, bus 1 is considered as slack bus, bus 2 & 3 are the generator buses, and buses 4, 5 & 6 as the load buses respectively. The computation was done in the two cases.

#### A. FVSI

1) FVSI enhancement using TCSC in inductive mode of compensation:

By the computation of indices it was found that the FVSI value is maximum for line 4-5, neglecting the generator buses, hence a TCSC was placed on line 4-5 and enhanced values of indices were calculated. From table 1 it is observed that for line 2-4 & 3-6 negative enhancement is taking place i.e. the values of indices with TCSC are higher than without TCSC. For line 3-5 a positive enhancement has occurred during 10% to 20% compensation, for 30% compensation no change in enhancement, & from 40% to 50% compensation a positive enhancement took place. For line 1-5, continuous increment in enhancement index was observed from 10% to 50% compensation as 43% to 147% respectively.

2) FVSI enhancement using TCSC in capacitive mode of compensation:

For line 2-4 a positive enhancement took place from 10% to 50% compensation and a continuous increment in enhancement index was observed from 78% to 325% respectively.

1) VQILine enhancement using TCSC in inductive mode of compensation:

By the computation of indices it was found that the FVSI value is maximum for line 5-6, neglecting the generator buses, hence a TCSC was placed on line 5-6 and enhanced values of indices were calculated. From table 2 lines 1-5, 2-5, 3-5, 4-5 2-6, 3-6 showed negative enhancements and a continuous increment in enhancement index can be observed from 10% to 50% compensation.

2) VQI<sub>Line</sub> enhancement using TCSC in capacitive mode of compensation:

From table 2 it can be observed that lines showed positive enhancement and a continuous increment in the values of enhancement index can be observed from 10% to 50% compensation.

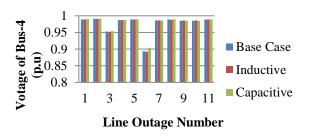
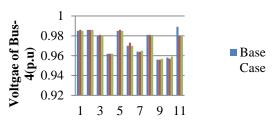


Fig7. Voltage Profile Bus 5

Fig. 7 shows Voltage Profile of bus-5 for base case, inductive and capacitive modes. From the above graph it can be observed that the voltage values are enhanced in the inductive & capacitive modes. The maximum variation in voltage at bus 4 is observed under 3-6 outage condition.



Line Outage Number

#### Fig8. Voltage Profile of Bus 4

The voltages Profile for bus - 4 for base case, inductive and capacitive modes are as shown above. It can be observed that the values of voltages are enhanced considerably in the capacitive mode. The maximum variation in voltage at bus 4 is observed under 2-4 outage condition.



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		INDUCTIVE COMPENSATION						CAPACITIVE COMPENSATION				
LINE OUTAGE	MAXIMUM BASE VALUE FVSI	10.00%	20.00%	30.00%	40.00%	50.00%	MAXIMUM BASE VALUE FVSI	10.00%	20.00%	30.00%	40.00%	50.00%
12	0.3042 (LINE 3-5)	15%	29%	40%	47%	55%	0.3042	-18%	-43%	-72%	-104%	-150%
14	0.3191 (LINE 3-5)	3%	10%	14%	21%	28%	0.3191	-3%	-3%	3%	10%	28%
15	0.3354 (LINE 3-5)	43%	76%	106%	130%	147%	0.3354	-52%	-117%	-196%	-293%	-413%
23	0.2928 (LINE 3-5)	19%	30%	42%	49%	57%	0.2928	-19%	-41%	-67%	-104%	-148%
24	0.4167 (LINE 1-4)	-73%	-144%	-210%	-272%	-331%	0.4167	78%	158%	243%	325%	408%
25	0.3430 (LINE 3-5)	13%	23%	29%	32%	35%	0.343	-16%	-38%	-67%	-108%	-161%
26	0.3242 (LINE 3-5)	24%	41%	58%	72%	82%	0.3242	-27%	-64%	-104%	-157%	-223%
35	0.3014 (LINE 2-5)	4%	4%	0%	-4%	-8%	0.3014	-8%	-20%	-44%	-76%	-123%
36	0.4386 (LINE 2-6)	-2%	-6%	-7%	-11%	-15%	0.4386	4%	6%	6%	6%	2%
56	0.3108 (LINE 3-5)	11%	21%	28%	32%	36%	0.3108	-14%	-32%	-56%	-84%	-123%

#### Table 1.FVSI % Enhancement

## Table 2.VQI % Enhancement

		INDUCTIVE COMPENSATION						CAPACITIVE COMPENSATION				
LINE OUTAGE	MAXIMUM BASE VALUE VQI	10.00%	20.00%	30.00%	40.00%	50.00%	MAXIMUM BASE VALUE VQI	10.00%	20.00%	30.00%	40.00%	50.00%
12	0.2508 (LINE 3-5)	-58%	-111%	-157%	-199%	-237%	0.2508	66%	143%	234%	337%	458%
14	0.2630 (LINE 3-5)	-58%	-113%	-156%	-200%	-236%	0.263	66%	144%	230%	328%	447%
15	0.2765 (LINE 3-5)	-56%	-110%	-158%	-203%	-244%	0.2765	59%	126%	198%	270%	347%
23	0.2414 (LINE 3-5)	-45%	-86%	-126%	-159%	-192%	0.2414	53%	114%	184%	259%	346%
24	0.3922 (LINE 1-4)	-13%	-24%	-33%	-44%	-52%	0.3922	13%	30%	46%	65%	88%
25	0.2827 (LINE 3-5)	-64%	-121%	-177%	-226%	-269%	0.2827	71%	150%	237%	332%	439%
26	0.2673 (LINE 3-5)	-64%	-121%	-170%	-213%	-252%	0.2673	72%	159%	254%	374%	511%
35	0.2713 (LINE 2-5)	-80%	-154%	-220%	-281%	-337%	0.2713	89%	188%	297%	422%	555%
36	0.3907 (LINE 2-6)	-52%	-103%	-154%	-206%	-253%	0.3907	50%	98%	137%	172%	191%
45	0.2348 (LINE 3-5)	-50%	-96%	-134%	-171%	-205%	0.2348	58%	121%	193%	270%	361%

## VII. CONCLUSION

Line Voltage Stability Indices gives an indication about <sup>[1]</sup> the system stability. The value of the indices helps in <sup>[2]</sup> identifying the most critical transmission line in the system. A TCSC is installed at the most severe line and as a result enhanced values of line voltage stability indices <sup>[3]</sup> i.e. FVSI and VQI are obtained. Thus it is observed that the severity of the line is reduced. Installation of TCSC at the most suitable location also improves the voltage profile of the buses. <sup>[5]</sup>

#### VIII. ACKNOWLEDGEMENT

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#### REFERENCES

- ] P.Kundur "Power System Stability and Control" McGraw-Hill, New York, 1994
- [2] Claudia Reis, F.P. Maciel Barbosa et al. "A Comparison of Voltage Stability indices" IEEE MELECON, pp. 1007-1010, May 16-19 2006, Benalmadena (Malaga), Spain.
- [3] C. Reis and F.P. Maciel Barbosa, "Line Indices for Voltage Stability Assessment" IEEE Bucharest Power Tech Conference, pp.1-6, June 28- 2 July 2009 Bucharest.
- [4] Alberto D. Del Rosso, Victor M. Dona "A Study of TCSC controller Design System Stability Improvement" IEEE Transactions On Power Systems Vol. 18 pp. 1487-1496 17 Nov 2003.
- [5] S. Meikandasivam, Rajesh Kumar Nema, Shailendra Kumar Jain "Selection of TCSC Parameters Inductive & Capacitive" IEEE IICPE Conference pp. 1-5, 28-30 Jan 2011, New Delhi
- [6] Ismail Musirin and TitikKhawa Abdul Rahman "Novel Fast Voltage Stability Index (FVSI) for Voltage Stability Analysis in Power Transmission System" IEEE Student Conference on Research and Development Proceedings, pp. 265-268, 2002
- [7] F.A. Althowibi, M.W. Mustafa "Line Voltage Stability Calculations in Power Systems" IEEE International conference on power and energy, pp. 396-401, Nov 29-Dec 1 2010 Kaula Lumpur, Malaysia.
   [8] M.W.Burnerd, O.
- [8] MatPower4.0